



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori et al.
Serial No. : 10/623,581
Filed : July 22, 2003
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM
TRANSISTOR

Art Unit : 2811
Examiner : Stanetta Isaac

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR TWO-MONTH EXTENSION OF TIME

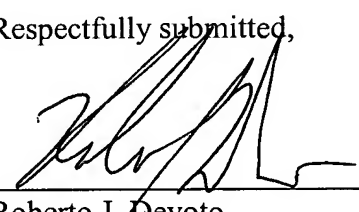
Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated July 13, 2005, be extended for two months to and including December 13, 2005.

Applicants submitted a \$120 fee with the response filed on November 14, 2005. Accordingly, please apply the \$330 (i.e., \$450-\$120) required fee and any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: _____

12/5/03


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